

GE60L02

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	25V
RDS(ON)	13mΩ
ID	50A

Description

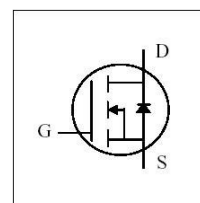
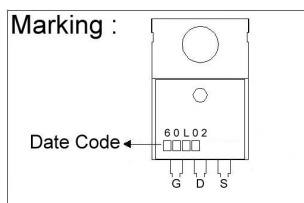
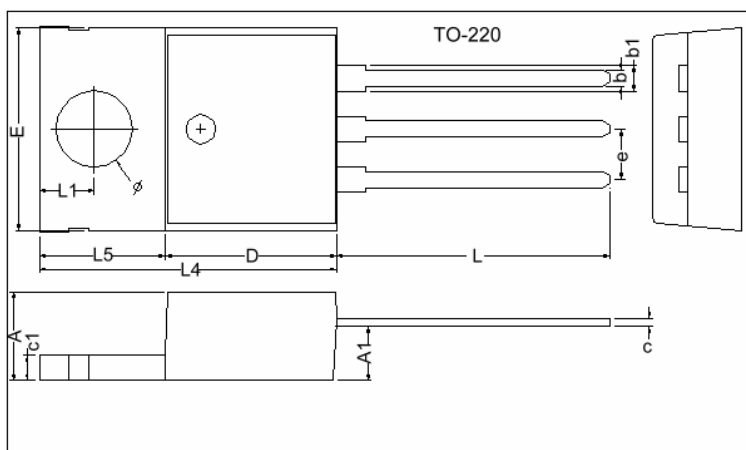
The GE60L02 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The through-hole version (TO-220) is available for low-profile applications and suited for low voltage applications such as DC/DC converters.

Features

- *Simple Drive Requirement
- *Low Gate Charge
- *Fast Switching

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.80	c1	1.25	1.45
b	0.76	1.00	b1	1.17	1.47
c	0.36	0.50	L	13.25	14.25
D	8.60	9.00	e	2.54 REF.	
E	9.80	10.4	L1	2.60	2.89
L4	14.7	15.3	Ø	3.71	3.96
L5	6.20	6.60	A1	2.60	2.80

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	25	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=25^{\circ}C$	50	A
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=100^{\circ}C$	32	A
Pulsed Drain Current ¹	I_{DM}	180	A
Total Power Dissipation	$P_D @T_C=25^{\circ}C$	62.5	W
Linear Derating Factor		0.5	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-case Max.	Rthj-case	2.0	°C/W
Thermal Resistance Junction-ambient Max.	Rthj-amb	62	°C/W

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	25	-	-	V	$V_{GS}=0, I_D=250\mu A$
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.037	-	V/°C	Reference to 25°C, $I_D=1mA$
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	3.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Forward Transconductance	g_{fs}	-	30	-	S	$V_{DS}=10V, I_D=25A$
Gate-Source Leakage Current	I_{GSS}	-	-	±100	nA	$V_{GS}= \pm 20V$
Drain-Source Leakage Current(T _j =25°C)	I_{DSS}	-	-	1	uA	$V_{DS}=25V, V_{GS}=0$
Drain-Source Leakage Current(T _j =150°C)		-	-	25	uA	$V_{DS}=20V, V_{GS}=0$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	-	13	mΩ	$V_{GS}=10V, I_D=25A$
		-	-	26		$V_{GS}=4.5V, I_D=20A$
Total Gate Charge ²	Q_g	-	21	-	nC	$I_D=25A$ $V_{DS}=20V$ $V_{GS}=5V$
Gate-Source Charge	Q_{gs}	-	2.8	-		
Gate-Drain ("Miller") Charge	Q_{gd}	-	16	-		
Turn-on Delay Time ²	$T_{d(on)}$	-	8	-	ns	$V_{DS}=15V$ $I_D=20A$ $V_{GS}=10V$ $R_G=3.3\Omega$ $R_D=0.75\Omega$
Rise Time	T_r	-	75	-		
Turn-off Delay Time	$T_{d(off)}$	-	22	-		
Fall Time	T_f	-	20	-		
Input Capacitance	C_{iss}	-	605	-	pF	$V_{GS}=0V$ $V_{DS}=25V$ $f=1.0MHz$
Output Capacitance	C_{oss}	-	415	-		
Reverse Transfer Capacitance	C_{rss}	-	195	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V_{SD}	-	-	1.26	V	$I_S=50A, V_{GS}=0V, T_j=25^\circ C$
Continuous Source Current (Body Diode)	I_S	-	-	50	A	$V_D=V_G=0V, V_S=1.26V$
Pulsed Source Current (Body Diode) ¹	I_{SM}	-	-	180	A	

Drain-Source Avalanche Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Single Pulse Avalanche Energy ²	E_{AS}	-	-	61	mJ	$V_{DD}=25V, I_D=35A, L=100\mu H$
Avalanche Current	I_{AR}	-	-	35	A	$V_{GS}=10V,$

Notes: 1. Pulse width limited by safe operating area.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

Characteristics Curve

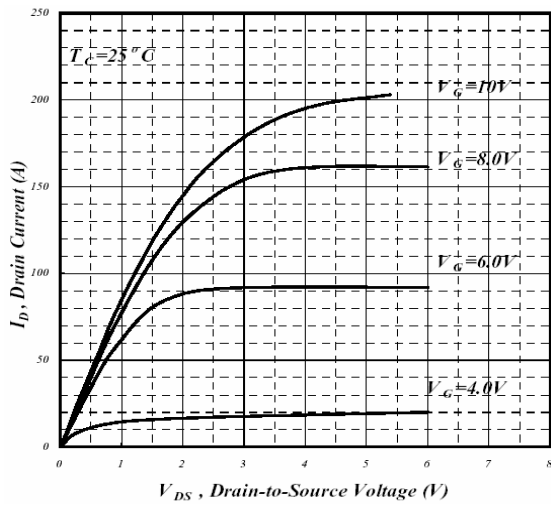


Fig 1. Typical Output Characteristics

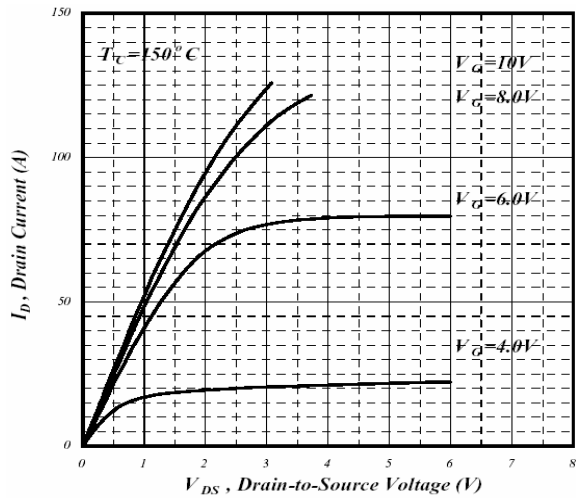


Fig 2. Typical Output Characteristics

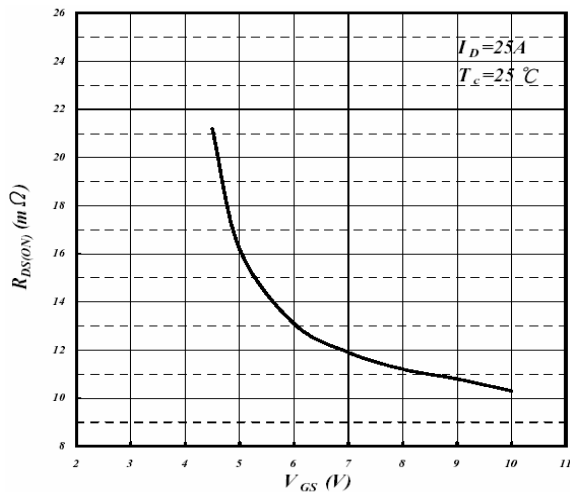


Fig 3. On-Resistance v.s. Gate Voltage

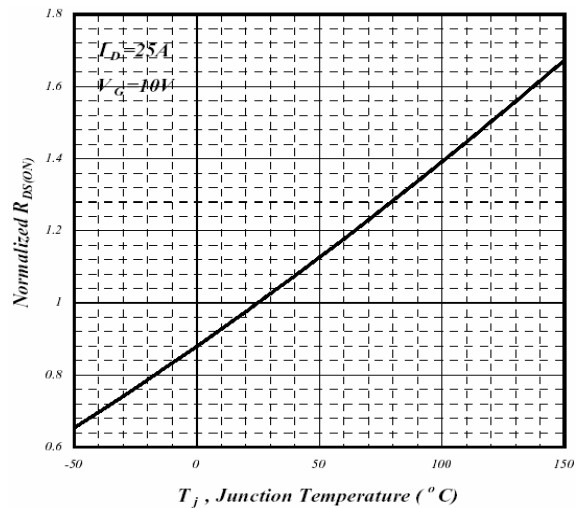


Fig 4. Normalized On-Resistance v.s. Junction Temperature

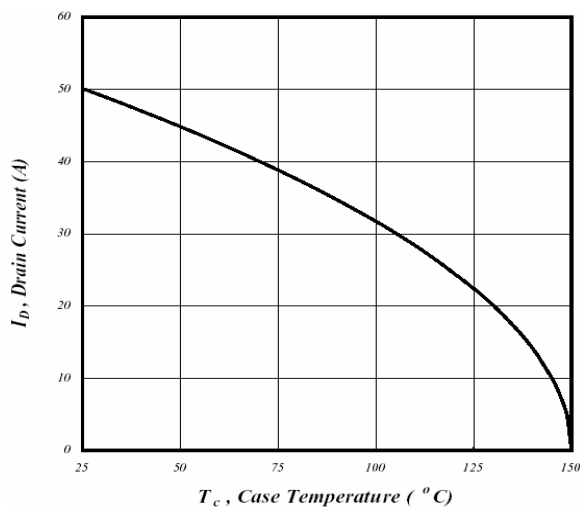


Fig 5. Maximum Drain Current v.s. Case Temperature

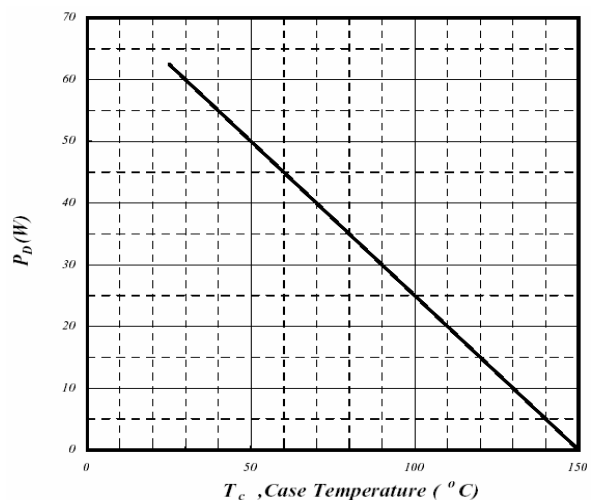


Fig 6. Type Power Dissipation

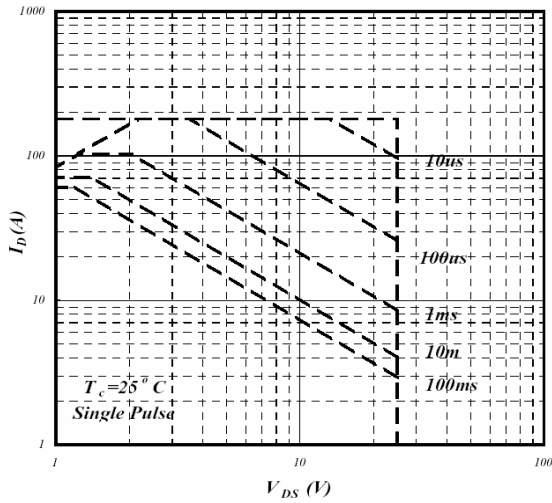


Fig 7. Maximum Safe Operating Area

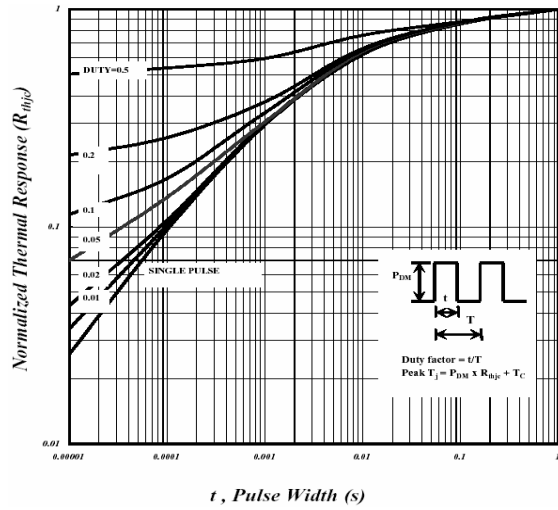


Fig 8. Effective Transient Thermal Impedance

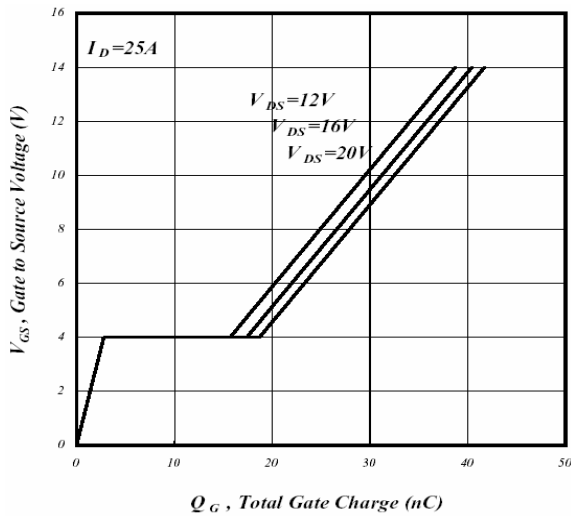


Fig 9. Gate Charge Characteristics

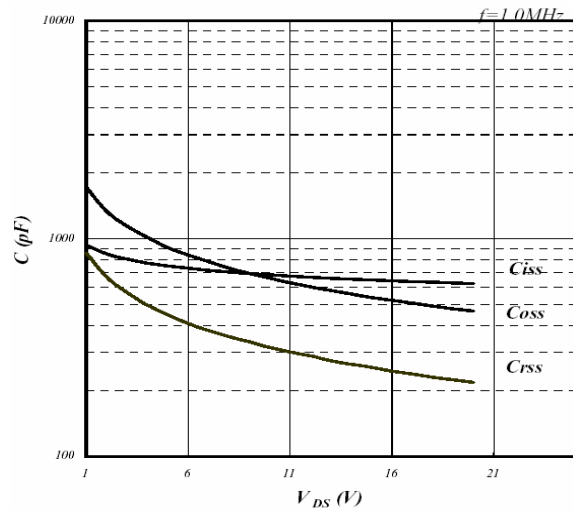


Fig 10. Typical Capacitance Characteristics

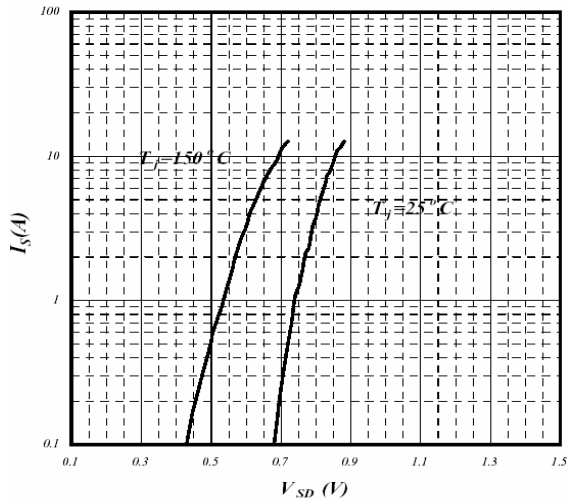


Fig 11. Forward Characteristics of Reverse Diode

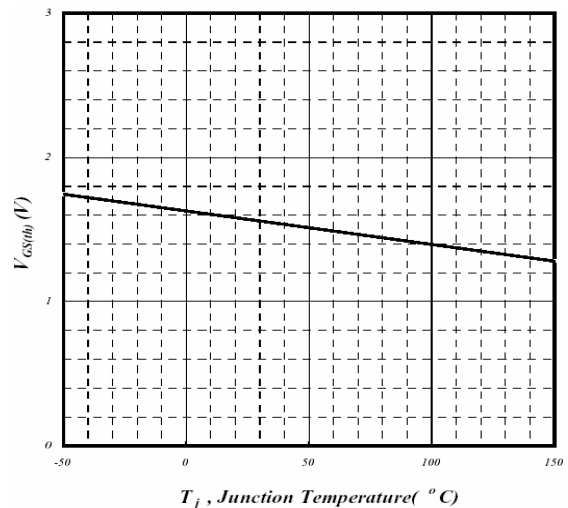


Fig 12. Gate Threshold Voltage v.s. Junction Temperature

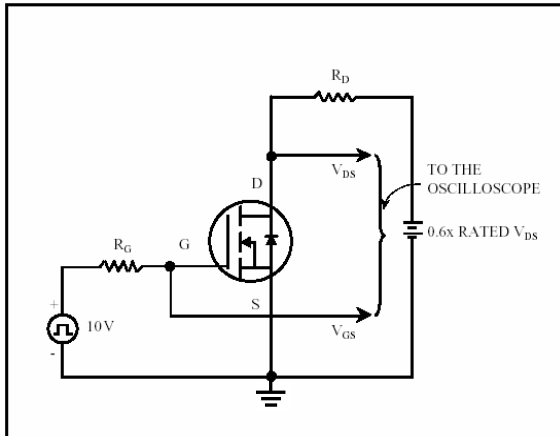


Fig 13. Switching Time Circuit

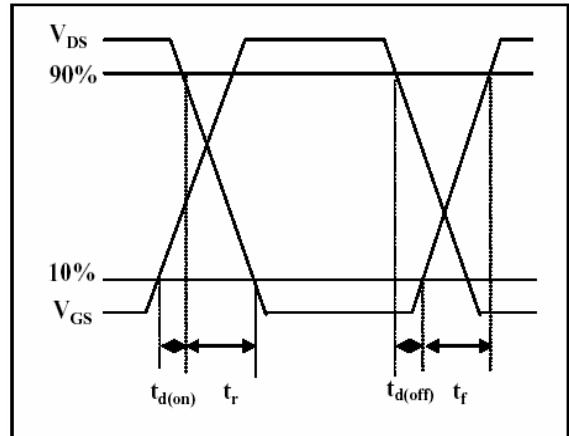


Fig 14. Switching Time Waveform

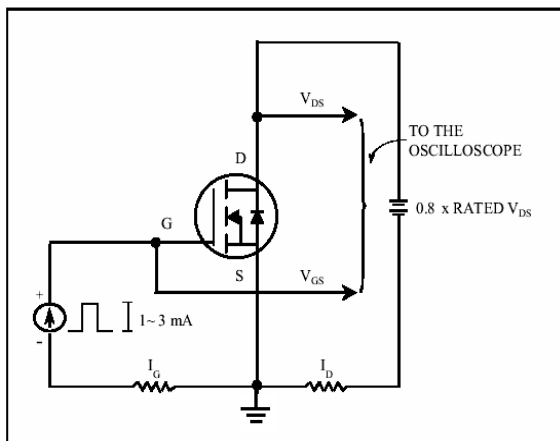


Fig 15. Gate Charge Circuit

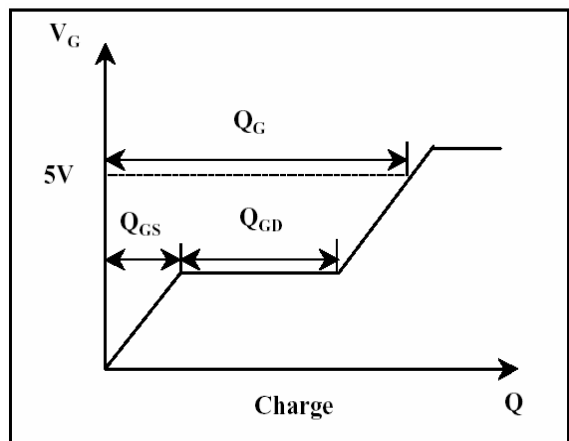


Fig 16. Gate Charge Waveform

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